

# VOLTAGE CONTROLLED OSCILLATOR

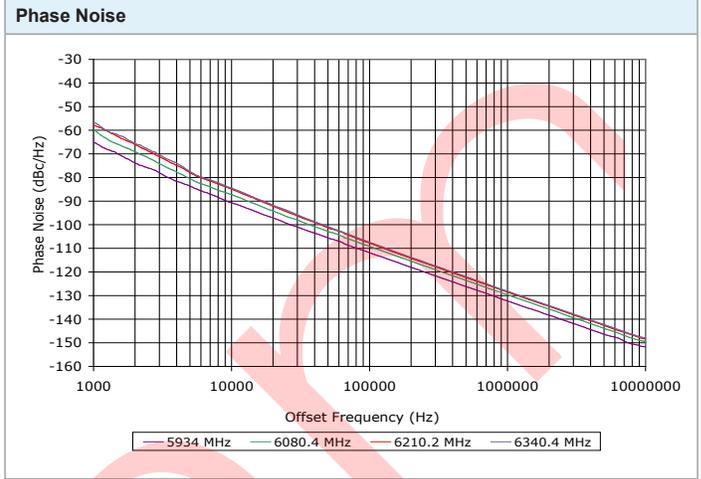
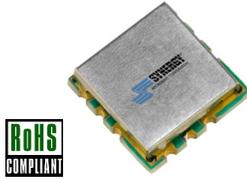
## SURFACE MOUNT MODEL: DCO608634-3

OPTIMIZED BANDWIDTH

6080 - 6340 MHz

**FEATURES:**

- ▶ Miniature Size, Surface Mount (0.3" x 0.3")
- ▶ Exceptional Phase Noise Performance
- ▶ Fast Tuning
- ▶ Planar Resonator Construction



**SPECIFICATIONS (Rev B 01/09/14)**

Frequency	6080 - 6340 MHz	
Tuning Voltage	See table below.	
Bias Voltage	+3 VDC @ 26 mA (Max.)	
Output Power	-6 dBm (Min.)	
Tuning Sensitivity	95 - 105 MHz/V (Typ.)	
Output Impedance	50 Ohms (Nom.)	
Harmonic Suppression	25 dB (Typ.)	
Frequency Pulling	30 MHz (Typ. @ 1.75:1 VSWR)	
Frequency Pushing	30 MHz/V (Typ.)	
Tuning Port Capacitance	1.5 pF (Typ.)	
Typical Phase Noise	Offset	dBc/Hz
	10 kHz	-86
	100 kHz	-108
Operating Temperature Range	-40 to +85 °C	

**Guaranteed Tuning Specifications**

Tuning Voltage ( V )	Frequency ( MHz )
0.5	6080 (Max.)
5	6340 (Min.)

**Absolute Maximum Ratings**

Storage Temp. Range	-55 to +125 °C
Bias Voltage	+3.3 V
Tuning Voltage	+7 V
DC Voltage Applied to RF Out	±25 V

**Package #** 343

The package drawings show a square component with dimensions: 0.300 x 0.300 inches. Pin locations are specified as 0.120, 0.060, 0.000, 0.120, 0.000, 0.120, 0.060, 0.000. Pin identifiers include RF, VT, VCC, and RF. The recommended PCB layout shows dimensions for the component and pads: 0.080, 0.200, 0.340, 0.045 SQ X 12 PLCS., 0.140, 0.080, 0.000, 0.140, 0.000. A note states: 'PIN AND PORT IDENTIFIERS ARE NOT MARKED (FOR REF. ONLY)'. Another note says: 'PACKAGE MOUNTING: SEE APPLICATION NOTE AN7300 & AN7302'. Tolerances on three decimal places are ± 0.015. Dimensions are in inches, and all unlabeled pins are to be grounded.

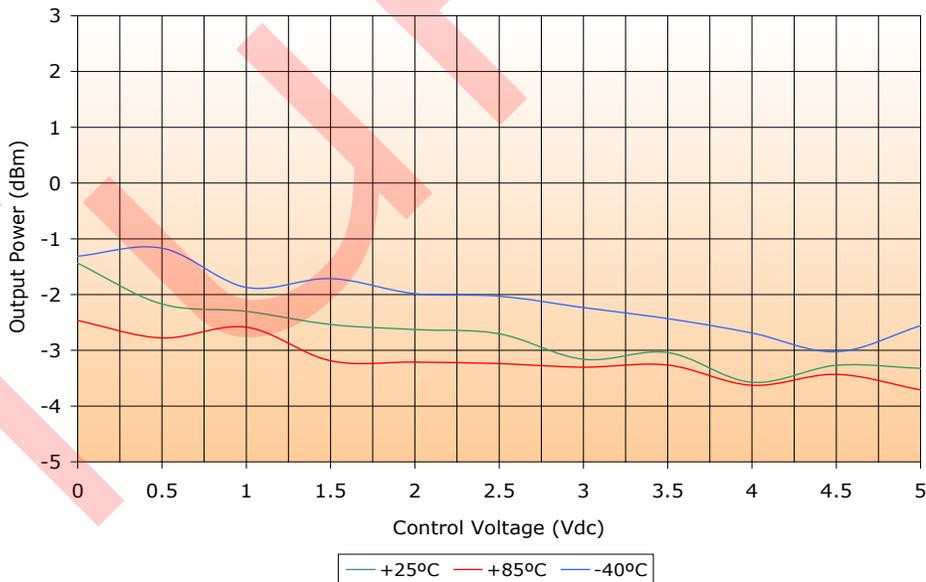
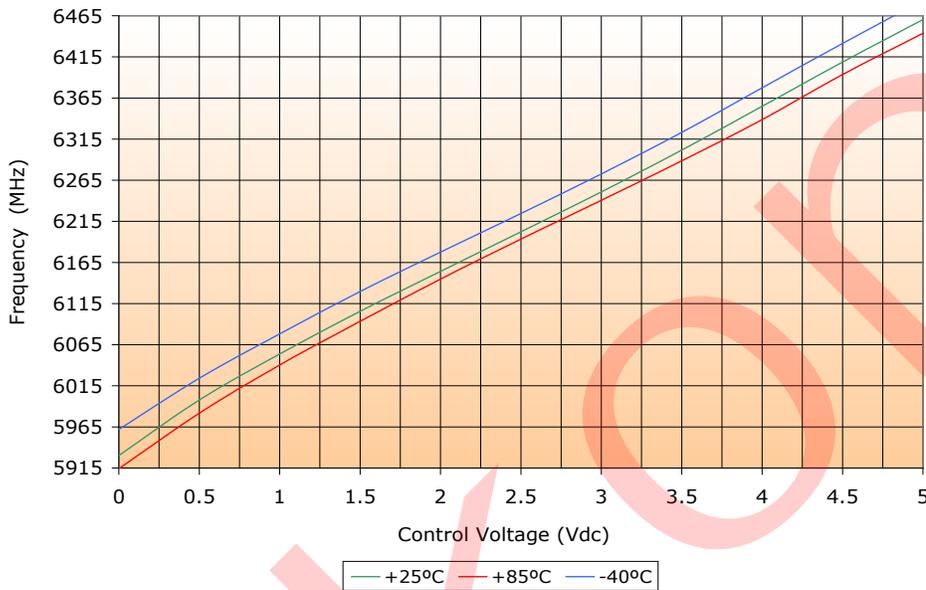
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